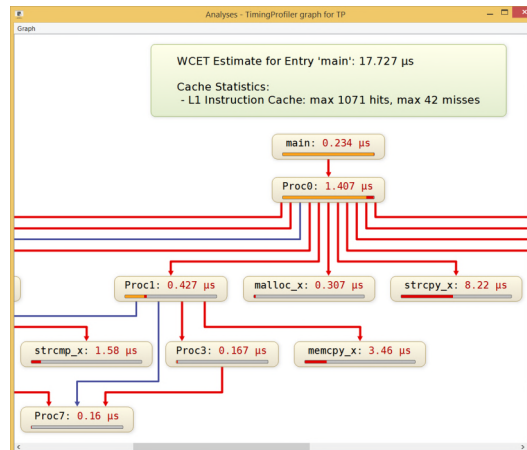# TimingProfiler

## Monitoring Timing Behavior During Code Development

TimingProfiler helps developers identify application parts that are causing unsatisfactory execution times. It is ideally suited for constantly monitoring timing behavior during software development and in model-based development environments. **TimingProfiler** delivers results as soon as there is compiled code. It can be used early in the process when measurements on physical hardware are costly or not even possible.

Global contributions

Call graph with timing information
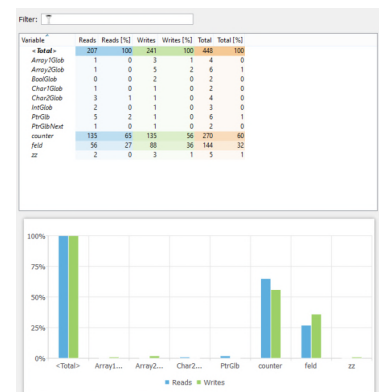
Usage of variables

Why do you need TimingProfiler?

- TimingProfiler helps address timing behavior continuously during development **from early stages on**.
- Developers can **immediately understand** the timing effects of their implementation decisions.
- TimingProfiler **visualizes** the call and control flow graph with timing information and displays relevant information about the executable.
- TimingProfiler computes worst-case execution time estimates for a slightly idealized model of the target processor. In contrast to aiT, it cannot derive guaranteed timing bounds, but efficiently computes **timing estimates**.
- **No access to physical hardware** and **no code instrumentation** are required.
- TimingProfiler automatically explores **all execution paths** in a program for all potential inputs.
- **No effort** is needed to set up and execute elaborate timing measurements.
- TimingProfiler can be **easily integrated** into the development process and used in **continuous test and integration** frameworks.
- Developers can **identify bottlenecks** early and **avoid late-stage integration problems**.

Supported processors and compilers

- TriCore/AURIX (Tasking/gcc/Diab)
- PowerPC (**CompCert**/Diab/gcc/GHS/CodeWarrior/DDC-1 Score)
- NEC/Renesas V850and RH850 (GHS/Diab/Renesas CS+)
- ARM (Cortex-M/Cortex-R) (**CompCert**/TI/ARM/gcc/GHS/ Tasking/Keil MDK-ARM/clang/Diab, GHS Ada)

- dsPIC (Microchip)
- MCS251 (Keil)
- LEON2/LEON3/LEON4 (gcc/GNAT)
- RISC-V (**CompCert**/gcc)
- Nios II (gcc)
- MIPS32 (gcc)

For further targets, please contact us.

AbsInt